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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. FEB. 11 2002 09/875,501 #6
Filing Date June 4, 2001
Inventor Klaus Florian Schuegraf et al. 2-14-02
Assignee Micron Technology, Inc.
Group Art Unit 2815 T. Flowers
Examiner E. Ortiz
Attorney's Docket No. MI22-1741
Title: Methods for Forming Wordlines, Transistor Gates, and Conductive
Interconnects, and Wordline, Transistor Gate, and Conductive Interconnect
Structures

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with
37 CFR §1.56. No admission is made regarding whether any of the submitted
references is prior art. Copies of the references are attached.

Respectfully Submitted:

Dated: 1-31-02

D. Brent Kenady
Reg. No. 40,045

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